

Notice of References Cited

Application/Control No.

10/814,561

Applicant(s)/Patent Under

Reexamination

CHOU, YU-SHENG

Examiner

Joshua D. Schneider

Art Unit

2182

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,292,852 B1	09-2001	Bodo et al.	710/20
*	B	US-5,084,789	01-1992	Kamo et al.	360/53
*	C	US-5,796,684 A	08-1998	Yaegashi, Akira	709/230
*	D	US-6,141,298 A	10-2000	Miller, David	369/30.33
*	E	US-6,453,391 B2	09-2002	Morita et al.	711/141
*	F	US-6,996,655 B1	02-2006	Lee et al.	710/308
*	G	US-6,556,769 B1	04-2003	Akahane et al.	386/46
*	H	US-5,237,466 A	08-1993	Glaser et al.	360/73.03
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Mano and Kime, Logic and Computer Design Fundamentals, 2000, Prentice Hall, 2nd Edition, pages 119-122
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.